Problems based on Pin Diagram of 8086

1) The clock rate of microprocessor 8086 is \_\_\_\_\_\_\_\_\_.

a) 5 MHZ

b) 8 MHZ

c) 10 MHZ

d) All of the mentioned -- correct

2)  In which T-state does the CPU sends the address to memory or I/O and the ALE signal for demultiplexing ­­­­­­\_\_\_\_\_\_\_\_.

a) T1 -- correct

b) T2

c) T3

d) T4

3) BHE of 8086 microprocessor signal is used to interface the\_\_\_\_\_

a) I/O

b) DMA

c) Even bank memory

d) Odd bank memory -- correct

4) Ready pin of a microprocessor is used\_\_\_\_\_\_

a) To indicate that processor is ready to receive inputs outputs

b) To indicate that processor is ready to receive inputs output

c) To introduce wait states -- correct

d) To provide direct memory access.

5)  The pins of minimum mode AD0-AD15 have \_\_\_\_\_ address and \_\_\_\_ data bus.

a) 16, 8

b) 16, 16 -- correct

c) 8, 16

d) 8, 8

6)  The \_\_\_\_\_\_\_ input is examined by a ‘wait’ instruction.

a) K

b) TEST -- correct

c) LOC

d) KIT

7) After reset execution starts form \_\_\_\_\_\_\_.

a) 00000H

b) FFFF0H -- correct

c) FFFFFH

d) 003FFH

8)  If there is an edge triggered input at NMI Pin causes\_\_\_\_\_ Interrupt.

a)  Type-0

b)  Type-1

c) Type-2 -- correct

d) Type-3

9) If MN/MX is low, the 8086 operates in \_\_\_\_\_ mode.

a) Minimum mode

b)  Maximum mode -- correct

c)  Both A and B

d) Control mode

10) The RD, WR, M/IO is the heart of control for a \_\_\_\_\_\_\_ mode.

a) Minimum mode -- correct

b)  Maximum mode

c)  Both A and B

d) Control mode

11) If S’2 =0, S’1 =1, S’0 =1, what is the status of the microprocessor?

a) Interrupt acknowledge

b) Read I/O port

c) Write I/O port

d) Halt -- correct

12) Which of the following processor supports pipelined architecture?

a) 8080

b) 8085

c) 8086 -- correct

d) 8008

13) In order to initiate the fetch cycle by BIU atleast \_\_\_\_\_\_\_ bytes of the queue must be empty.

a) 1

b) 2 -- correct

c) 3

d) 4

14)  For TEST signal to be accepted, it must be low for atleast\_\_\_\_\_\_ clock cycles.

a) 3

b) 4

c) 5 -- correct

d) 6

15) In 8086, examples of Non-Maskable Interrupts are

a) TRAP -- correct

b) RST 6.5

c) INTR

d) None of these

16) In 8086 microprocessor the following has the highest priority among all type interrupts.

a) NMI -- correct

b) DIV 0

c) Type 255

d) Overflow